



# M-CORD Demo Architecture at MWC 2018

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An Operator Led Consortium

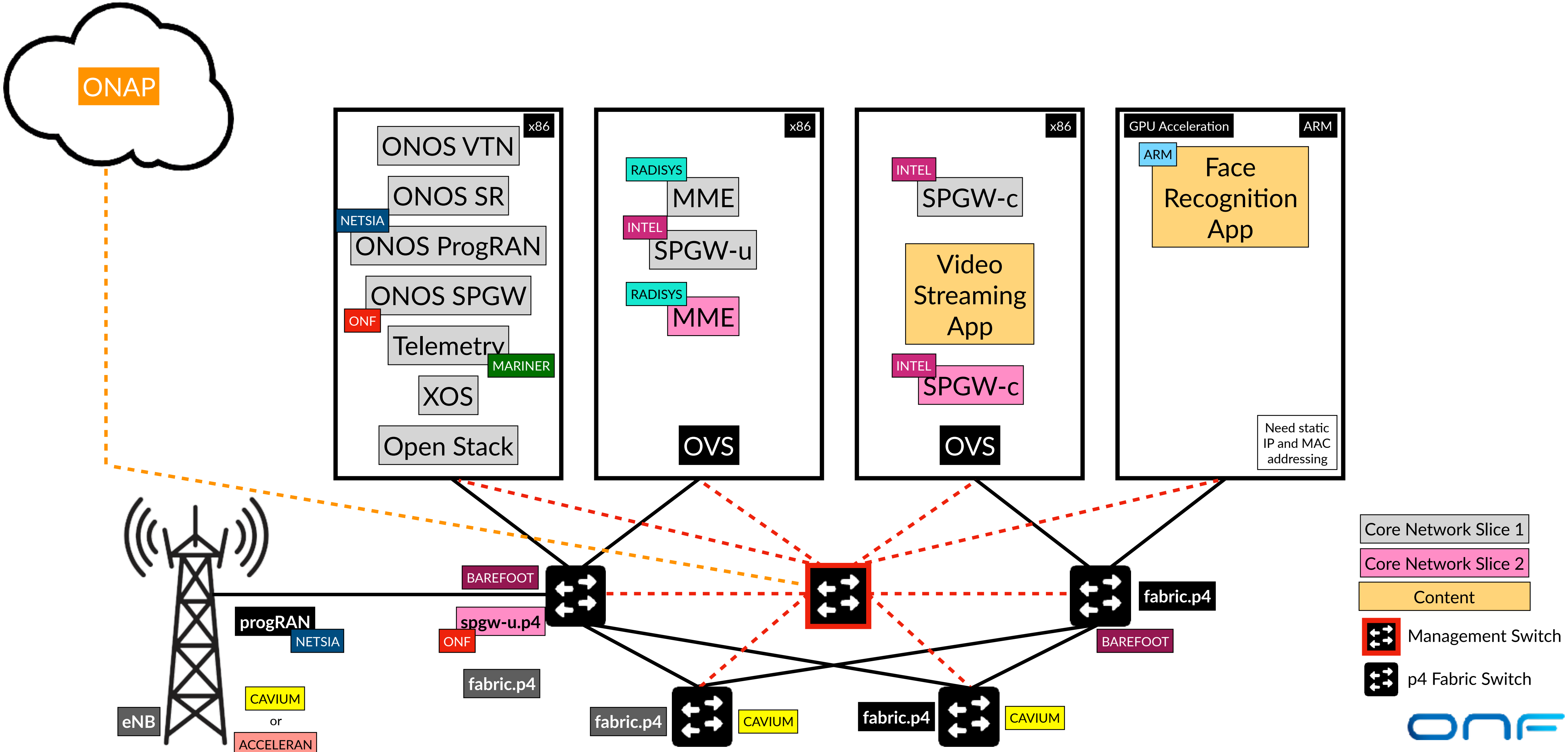


# Overview

MWC demonstration will highlight these major points:

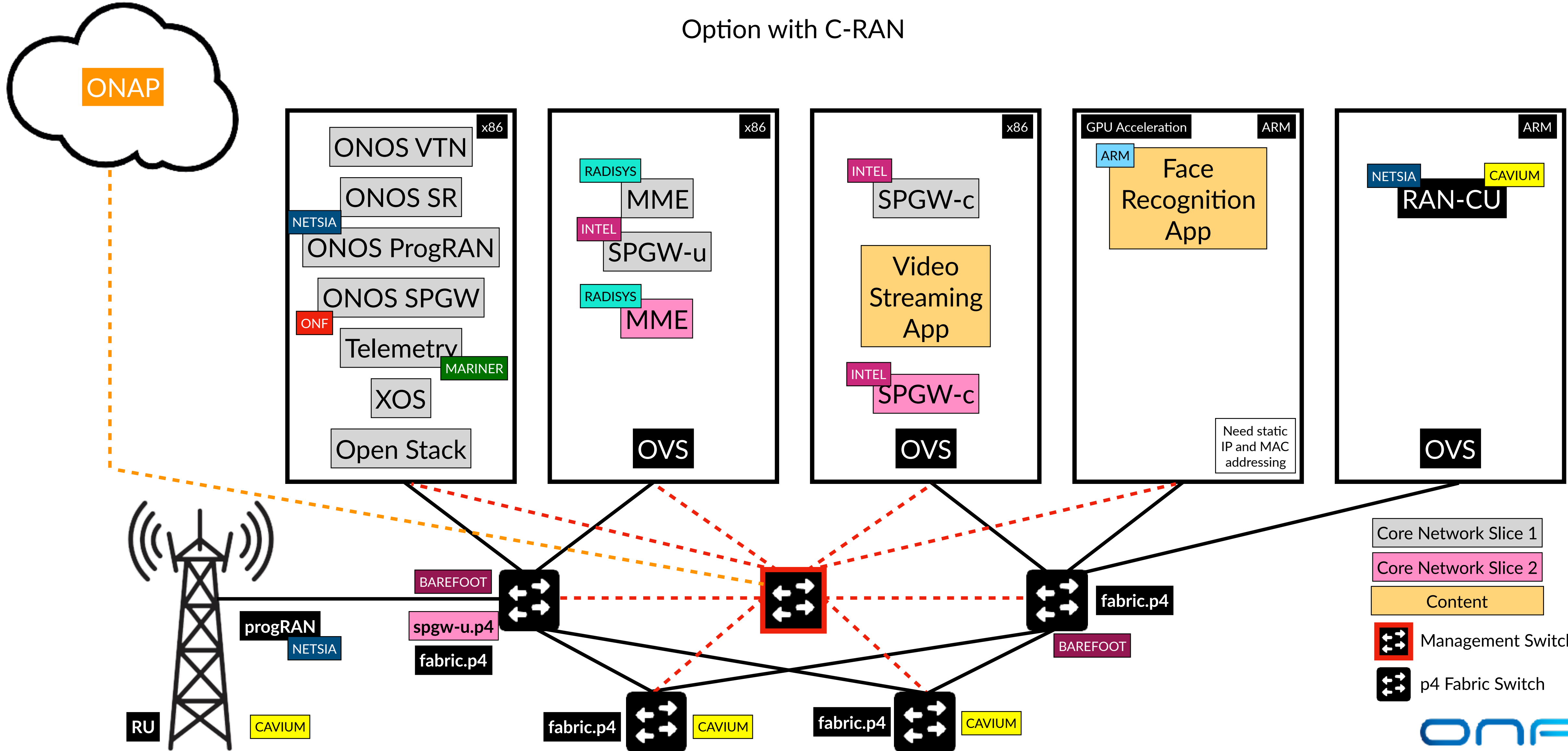
- **p4** fabric with 3GPP user plane functions realized as p4 programs running on programmable switch silicon
- capability of running a VNF (Face Recognition) on a NIC card enabling **GPU acceleration**
- **ONAP integration**
  - designing an SDN-application (ProgRAN) on ONAP and managing its run-time in M-CORD
  - configuring and instantiating a VNF (Face Recognition) on ONAP-managed COTS server that is connected to the M-CORD fabric
- basic **telemetry** capability, interfacing ONOS and Open Stack, with closed-loop analytics
- **e2e network slicing** where one slice has an all-VNF EPC and the other has p4 UP-based EPC. Multiple RAN slices may be tied to each one of the two Core Network slices.

# MWC Demo Architecture



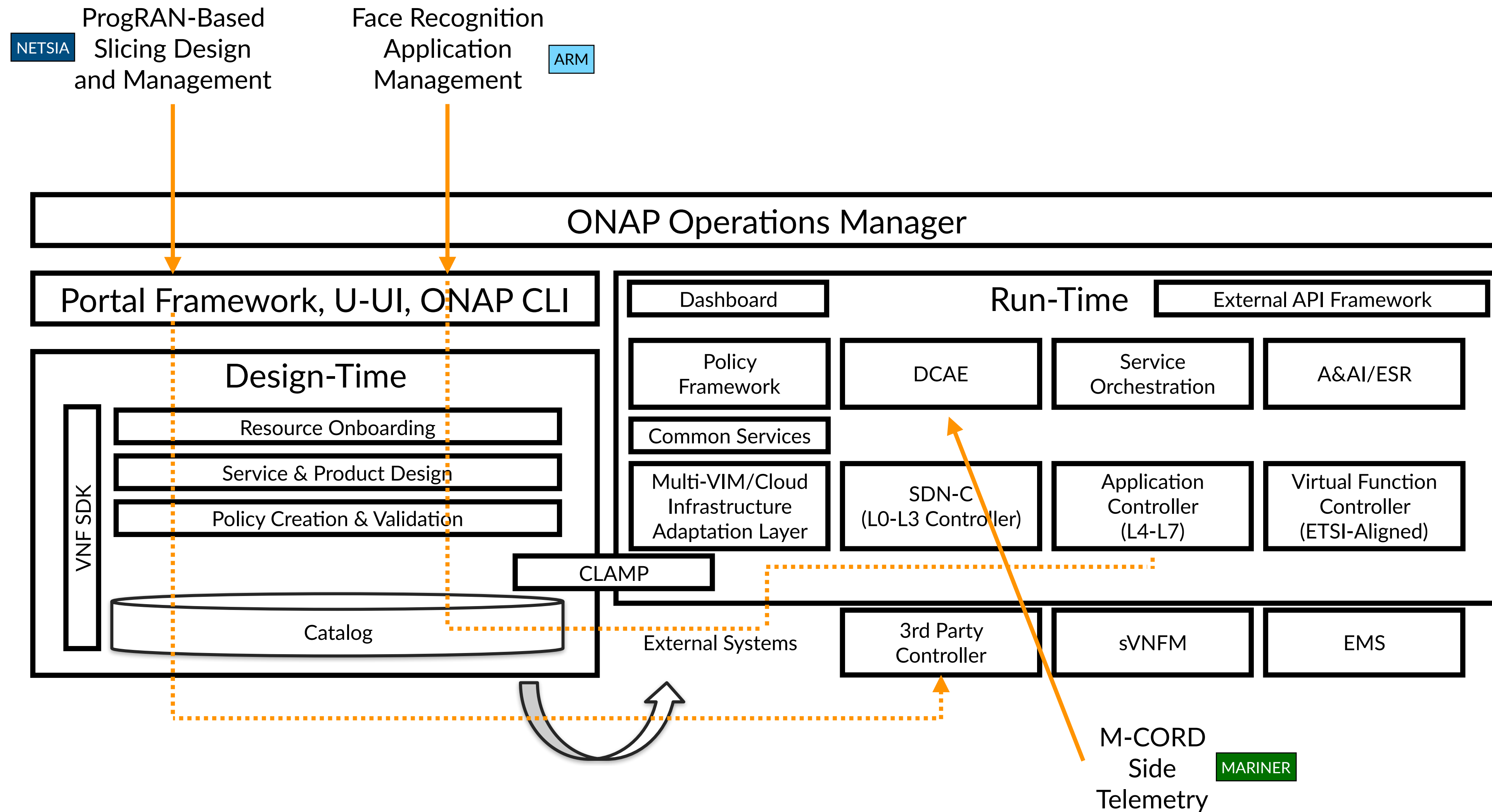
# MWC Demo Architecture

Option with C-RAN



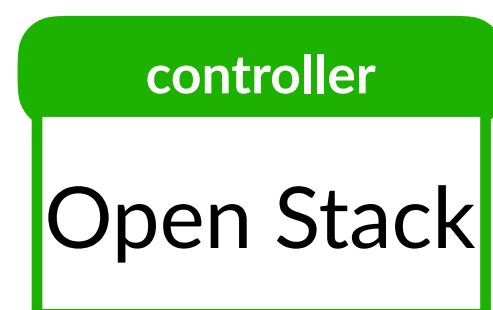
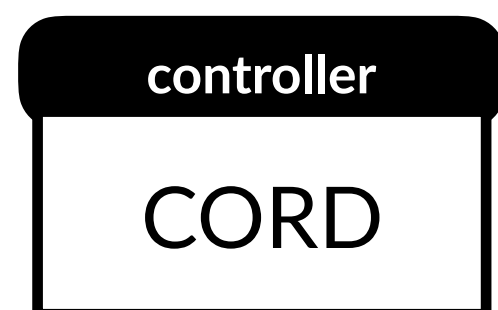
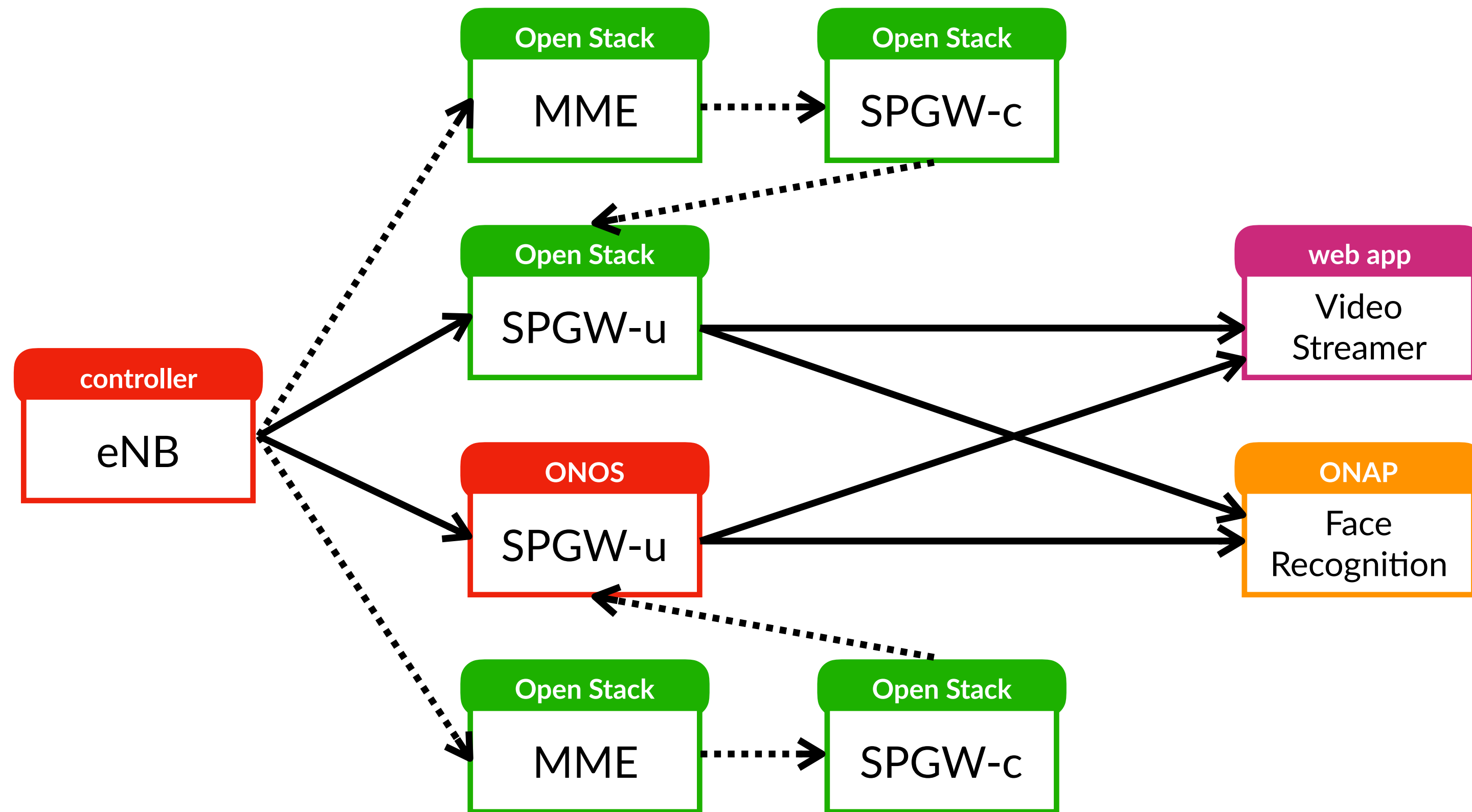


# ONAP Integration



# Service Graph

Two Core Network Slices and All Content Reachable from Both



# Service Graph

Telemetry Service Interfacing with ONOS, Open Stack for Data Collection and ONAP for Reporting

